

Electronic Acknowledgement Receipt

EFS ID:	1107875
Application Number:	10749912
Confirmation Number:	1387
Title of Invention:	Instruction memory hierarchy for an embedded processor
First Named Inventor:	Donald E. Steiss
Customer Number:	758
Filer:	Dorian Cartwright
Filer Authorized By:	
Attorney Docket Number:	22347-08261 (8117)
Receipt Date:	10-JUL-2006
Filing Date:	30-DEC-2003
Time Stamp:	23:55:18
Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
------------------------	----

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1	Amendment - After Non-Final Rejection	8261_AmdA_efile.pdf	379403	no	16

Warnings:	
Information:	
Total Files Size (in bytes):	379403
This Acknowledgement Receipt evidences receipt on the noted date by the USPTO of the indicated documents, characterized by the applicant, and including page counts, where applicable. It serves as evidence of receipt similar to a Post Card, as described in MPEP 503.	
<u>New Applications Under 35 U.S.C. 111</u> If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.	
<u>National Stage of an International Application under 35 U.S.C. 371</u> If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.	